Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	930	active adj resist$4	US-PGPUB; USPAT	OR	OFF	2005/03/10 08:39
L2	36	1 with linear$3	US-PGPUB; USPAT	OR	OFF	2005/03/10 08:58
L3	10	1 with (triode or ohmic)	US-PGPUB; USPAT	OR	OFF	2005/03/10 09:00
L4	111	1 with (MOS or MOSFET or FET)	US-PGPUB; USPAT	OR	OFF	2005/03/10 09:01
L5	77	1 with (PMOS or NMOS)	US-PGPUB; USPAT	OR	OFF	2005/03/10 09:01
L6	66	1 with ((P or N) adj (MOS or type or channel or FET))	US-PGPUB; USPAT	OR	OFF	2005/03/10 09:02
L7	132	(4 or 5 or 6) not (2 or 3)	US-PGPUB; USPAT	OR	OFF	2005/03/10 09:47
L8	150	1 and "327"/$.ccls.	US-PGPUB; USPAT	OR	OFF	2005/03/10 09:47
L9	21	1 and "323"/$.ccls.	US-PGPUB; USPAT	OR	OFF	2005/03/10 09:47
L10	98	(8 or 9) not (2 or 3 or 7)	US-PGPUB; USPAT	OR	OFF	2005/03/10 10:04
L11	2516	327/540,541,543,545,546.ccls.	US-PGPUB; USPAT	OR	OFF	2005/03/10 10:05
L12	1980	323/313,314,315.ccls.	US-PGPUB; USPAT	OR	OFF	2005/03/10 10:05
L13	4133	11 or 12	US-PGPUB; USPAT	OR	OFF	2005/03/10 10:05
L14	4118	13 not (2 or 3 or 7 or 10)	US-PGPUB; USPAT	OR	OFF	2005/03/10 10:05
L15	57	14 and (active near3 resist$4)	US-PGPUB; USPAT	OR	OFF	2005/03/10 10:17
L16	1090	14 and linear$3	US-PGPUB; USPAT	OR	OFF	2005/03/10 10:17
L17	1863	14 and (current adj mirror$1)	US-PGPUB; USPAT	OR	OFF	2005/03/10 10:17
L18	669	16 and 17	US-PGPUB; USPAT	OR	OFF	2005/03/10 10:18
L19	634	18 and resist$4	US-PGPUB; USPAT	OR	OFF	2005/03/10 10:18
L21	297	14 and ((multiple or plural$3) with (transistor$1 or PMOS or NMOS or MOSFET$1 or FET$1) with series)	US-PGPUB; USPAT	OR	OFF	2005/03/10 12:48

L22	1378	14 and ((second or 2nd or "2.sup. nd" or "2" or two) with (transistor$1 or PMOS or NMOS or MOSFET$1 or FET$1) with series)	US-PGPUB; USPAT	OR	OFF	2005/03/10 12:48
L23	259	19 and (21 or 22)	US-PGPUB; USPAT	OR	OFF	2005/03/10 10:21
L24	179	23 and gate and drain and source	US-PGPUB; USPAT	OR	OFF	2005/03/10 11:38
L25	211	23 and (reference near3 (voltage or potential))	US-PGPUB; USPAT	OR	OFF	2005/03/10 12:46
L26	87	23 and (vref or "v.sub.ref" or vr or "v.sub.r" or vint or "v.sub.int")	US-PGPUB; USPAT	OR	OFF	2005/03/10 12:46
L27	147	24 and (25 or 26)	US-PGPUB; USPAT	OR	OFF	2005/03/10 10:48
L28	32	24 not 27	US-PGPUB; USPAT	OR	OFF	2005/03/10 11:30
L29	80	23 not 24	US-PGPUB; USPAT	OR	OFF	2005/03/10 11:38
L30	375	19 not 23	US-PGPUB; USPAT	OR	OFF	2005/03/10 11:38
L31	237	30 and gate and drain and source	US-PGPUB; USPAT	OR	OFF	2005/03/10 11:39
L32	54	31 and (linear adj region$1)	US-PGPUB; USPAT	OR	OFF	2005/03/10 11:55
L33	183	31 not 32	US-PGPUB; USPAT	OR	OFF	2005/03/10 11:55
L34	151	33 and (reference near3 (voltage or potential))	US-PGPUB; USPAT	OR	OFF	2005/03/10 11:55
L35	76	33 and (vref or "v.sub.ref" or vr or "v.sub.r" or vint or "v.sub.int")	US-PGPUB; USPAT	OR	OFF	2005/03/10 12:08
L36	76	34 not 35	US-PGPUB; USPAT	OR	OFF	2005/03/10 12:19
L37	31	31 not (32 or 35 or 36)	US-PGPUB; USPAT	OR	OFF	2005/03/10 12:24
L38	138	30 not 31	US-PGPUB; USPAT	OR	OFF	2005/03/10 12:35
L39	0	19 not (27 or 28 or 29 or 30)	US-PGPUB; USPAT	OR	OFF	2005/03/10 12:36
L40	35	18 not 19	US-PGPUB; USPAT	OR	OFF	2005/03/10 12:40
L41	705	16 and mirror$3	US-PGPUB; USPAT	OR	OFF	2005/03/10 12:40
L42	36	41 not 17	US-PGPUB; USPAT	OR	OFF	2005/03/10 12:46
L43	1194	17 not 18	US-PGPUB; USPAT	OR	OFF	2005/03/10 12:46

L44	688	43 and (gate same drain same source)	US-PGPUB; USPAT	OR	OFF	2005/03/10 12:46
L45	567	44 and (reference near3 (voltage or potential))	US-PGPUB; USPAT	OR	OFF	2005/03/10 12:46
L46	277	44 and (vref or "v.sub.ref" or vr or "v.sub.r" or vint or "v.sub.int")	US-PGPUB; USPAT	OR	OFF	2005/03/10 12:47
L47	274	45 and 46	US-PGPUB; USPAT	OR	OFF	2005/03/10 12:47
L48	0	47 and linear	US-PGPUB; USPAT	OR	OFF	2005/03/10 12:47
L49	34	47 and ((multiple or plural$3) with (transistor$1 or PMOS or NMOS or MOSFET$1 or FET$1) with series)	US-PGPUB; USPAT	OR	OFF	2005/03/10 12:48
L50	117	47 and ((second or 2nd or "2.sup. nd" or "2" or two) with (transistor$1 or PMOS or NMOS or MOSFET$1 or FET$1) with series)	US-PGPUB; USPAT	OR	OFF	2005/03/10 12:49
L51	123	49 or 50	US-PGPUB; USPAT	OR	OFF	2005/03/10 13:13
L52	151	47 not 51	US-PGPUB; USPAT	OR	OFF	2005/03/10 13:41
L53	296	(45 or 46) not 47	US-PGPUB; USPAT	OR	OFF	2005/03/10 13:42
L54	269	53 and resist$4	US-PGPUB; USPAT	OR	OFF	2005/03/10 13:43
L55	218	54 and (NMOS or (N adj (MOS or channel or type)))	US-PGPUB; USPAT	OR	OFF	2005/03/10 13:46
L56	51	54 not 55	US-PGPUB; USPAT	OR	OFF	2005/03/10 13:43
L57	112	54 and (resist$4 with (NMOS or (N adj (MOS or channel or type))))	US-PGPUB; USPAT	OR	OFF	2005/03/10 14:08
L58	106	54 not (56 or 57)	US-PGPUB; USPAT	OR	OFF	2005/03/10 14:09